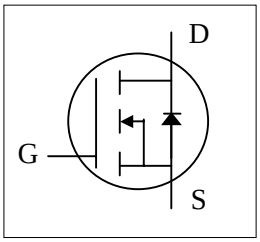
AP95T10AGW-HF

Halogen-Free Product

Advanced Power Electronics Corp.

N-CHANNEL ENHANCEMENT MODE POWER MOSFET

- ▼ **Simple Drive Requirement**
- ▼ **Lower On-resistance**
- ▼ **RoHS Compliant & Halogen-Free**

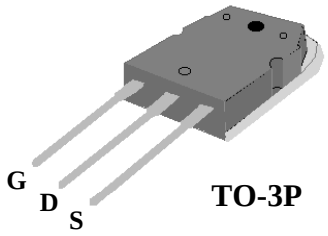


BV_{DSS}	100V
$R_{DS(ON)}$	6mΩ
I_D	150A

Description

AP95T10A series are from Advanced Power innovated design and silicon process technology to achieve the lowest possible on-resistance and fast switching performance. It provides the designer with an extreme efficient device for use in a wide range of power applications.

The TO-3P package is widely preferred for commercial-industrial surface mount applications and suited for higher voltage applications such as SMPS.



TO-3P

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	±20	V
I_D@T_C=25℃	Continuous Drain Current (Chip)	150	A
I_D@T_C=25℃	Continuous Drain Current, V_{GS} @ 10V[3]	120	A
I_D@T_C=100℃	Continuous Drain Current, V_{GS} @ 10V	95	A
I_{DM}	Pulsed Drain Current[1]	480	A
P_D@T_C=25℃	Total Power Dissipation	277.8	W
P_D@T_A=25℃	Total Power Dissipation	3.1	W
T_{STG}	Storage Temperature Range	-55 to 150	℃
T_J	Operating Junction Temperature Range	-55 to 150	℃

Thermal Data

Symbol	Parameter	Value	Units
Rthj-c	Maximum Thermal Resistance, Junction-case	0.45	℃/W
Rthj-a	Maixmum Thermal Resistance, Junction-ambient	40	℃/W

Electrical Characteristics@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_D=250uA	100	-	-	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=10V, I_D=60A	-	-	6	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=250uA	2	-	5	V
g_{fs}	Forward Transconductance	V_{DS}=10V, I_D=60A	-	120	-	S
I_{DSS}	Drain-Source Leakage Current	V_{DS}=100V, V_{GS}=0V	-	-	25	uA
I_{GSS}	Gate-Source Leakage	V_{GS}= ±20V, V_{DS}=0V	-	-	±100	nA
Q_g	Total Gate Charge	I_D=40A	-	170	270	nC
Q_{gs}	Gate-Source Charge	V_{DS}=80V	-	30	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	V_{GS}=10V	-	78	-	nC
$t_{d(on)}$	Turn-on Delay Time	V_{DS}=50V	-	130	-	ns
t_r	Rise Time	I_D=40A	-	250	-	ns
$t_{d(off)}$	Turn-off Delay Time	R_G=25Ω	-	360	-	ns
t_f	Fall Time	V_{GS}=10V	-	270	-	ns
C_{iss}	Input Capacitance	V_{GS}=0V	-	9060	14500	pF
C_{oss}	Output Capacitance	V_{DS}=25V	-	830	-	pF
C_{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	430	-	pF
R_g	Gate Resistance	f=1.0MHz	-	2.1	4.2	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage[2]	I_S=40A, V_{GS}=0V	-	-	1.3	V
t_{rr}	Reverse Recovery Time	I_S=10A, V_{GS}=0V	-	80	-	ns
Q_{rr}	Reverse Recovery Charge	dI/dt=100A/μs	-	270	-	nC

Notes:

1.Pulse width limited by Max. junction temperature.

2.Pulse test

3.Package limitation current is 120A.

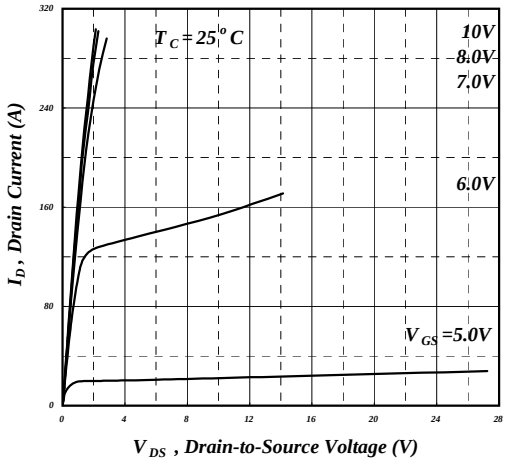


Fig 1. Typical Output Characteristics

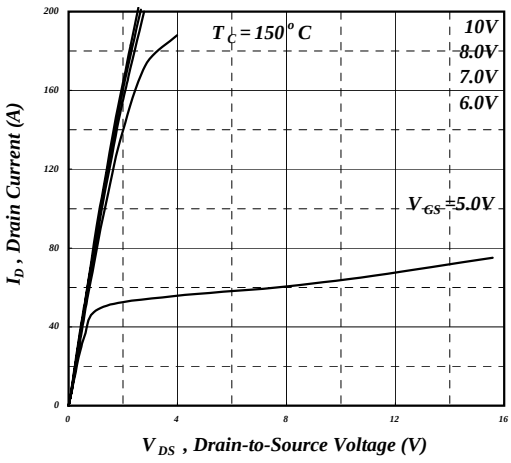


Fig 2. Typical Output Characteristics

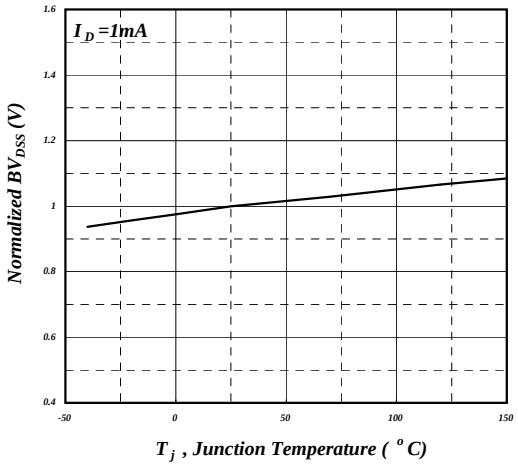


Fig 3. Normalized BV_{DSS} v.s. Junction Temperature

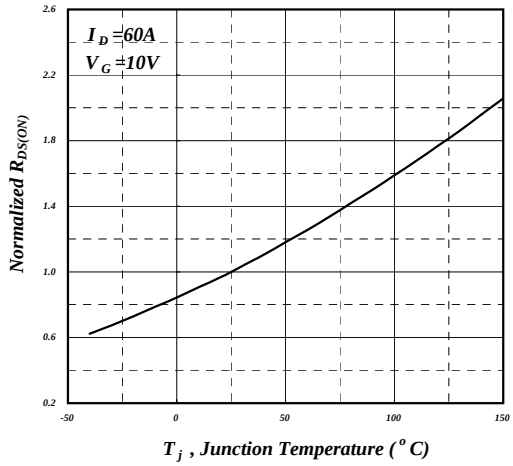


Fig 4. Normalized On-Resistance v.s. Junction Temperature

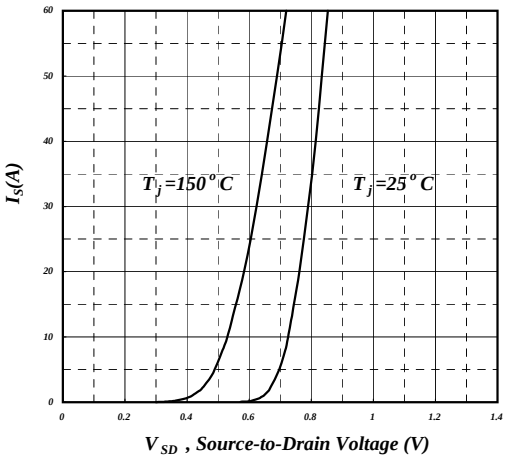


Fig 5. Forward Characteristic of Reverse Diode

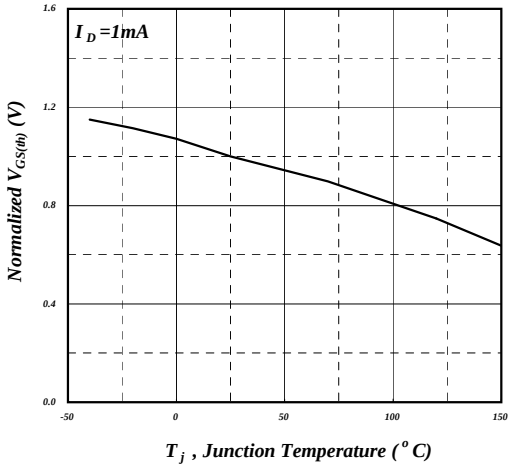


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

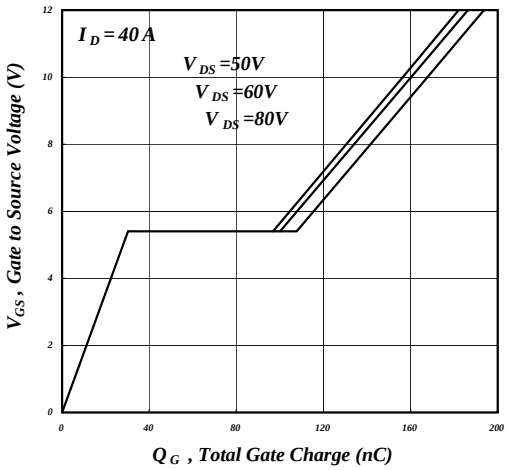


Fig 7. Gate Charge Characteristics

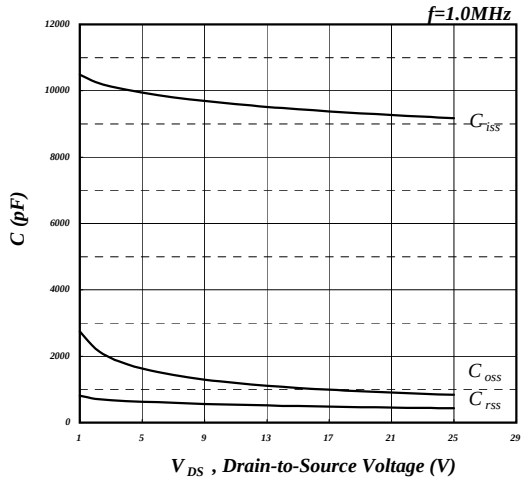


Fig 8. Typical Capacitance Characteristics

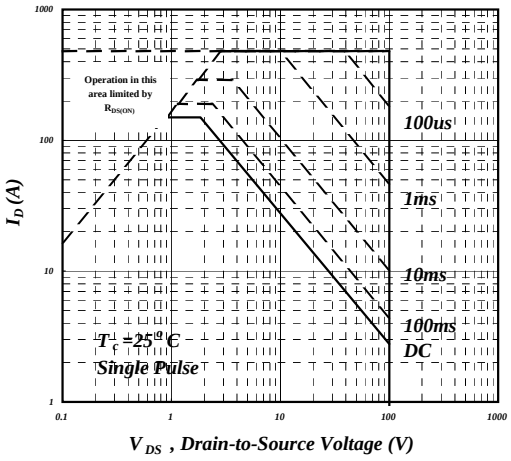


Fig 9. Maximum Safe Operating Area

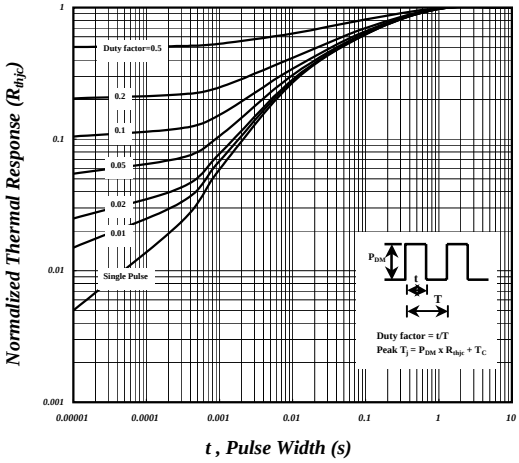


Fig 10. Effective Transient Thermal Impedance

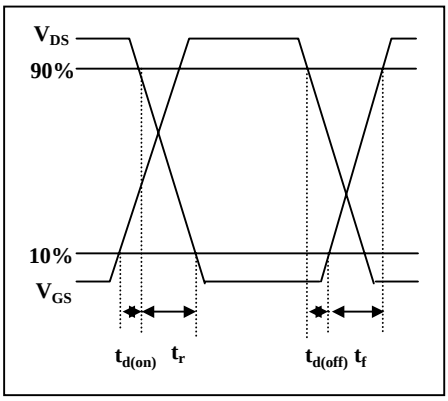


Fig 11. Switching Time Waveform

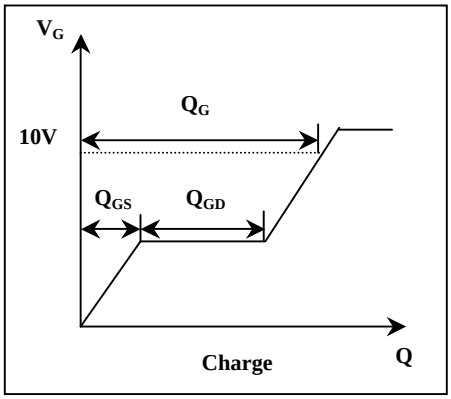


Fig 12. Gate Charge Waveform